



**RESPONSE UNDER 37 C.F.R. 1.116 EXPEDITED
PROCEDURE EXAMINING GROUP 2813**

Attorney's Docket No. 5649-1147

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Kim et al.

Serial No.: 10/706,755

Filed: November 12, 2003

For: INTEGRATED CIRCUIT STRUCTURES INCLUDING EPITAXIAL SILICON LAYERS IN ACTIVE REGIONS WITH A PLURALITY OF GATES THEREON

Confirmation No.: 7540

Group Art Unit: 2813

Examiner: Stephen W. Smoot

September 28, 2005

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT AFTER FINAL AND INTERVIEW SUMMARY

D.K.
Sir:

To
Enter
J.W.S.
10-5-05

Responsive to the Office Action dated August 10, 2005, please amend the above-identified Application as shown. It is not believed that an extension of time and/or additional fee(s), including fees for additional claims, are required, beyond those that may otherwise be provided for in documents accompanying this paper. In the event, however, that an extension of time is necessary to allow consideration of this paper, such an extension is hereby petitioned under 37 C.F.R. §1.136(a). Any additional fees believed to be due in connection with this paper may be charged to our Deposit Account No. 50-0220.

A Listing of the Claims begins on page 2 of this paper.

Remarks/Arguments begin on page 4 of this paper.